

Please note: Not all of the versions listed have been released or are planned for release.



IO-Link Master FPGA History (1800471)

Revision: 1.03

What Changed: Same as V1.11. Only filename changed.

Date: 06/24/16

Revision: 1.02

What Changed: Uses top_ioIm_dr_8_pnio_V07.bit.

Changed s1miso to drive hi for ~40 nsec after lo to hi transition

Changed s1miso output drive from 4 to 8 mA to be the same as other devices driving the same net.

Above function verified using the ISIM simulator rstn_o output and checking the schematic rtl view for correct signals.

Date: 08/06/15

Revision: 1.01

What Changed: Uses top_ioIm_dr_8_pnio_V06.bit.

Added pullup to sdo & sdo_dg inputs

Changed s1miso to open drain

Added functions to testbench to test the above changes.

Date: 08/05/15

Revision: 1.00

What Changed: Initial release.

Date: 04/29/15